

Atma Malik Institute of Technology & Research (AMRIT)

Vishwatmak Jangli Maharaj Ashram Trust's

Department of Electronics & Telecommunication Engineering

Academic Year: 2022-23 Semester: EVEN List of Guide Allocation Date: 11-7-2022 ACAD-DI-24B Rev: 00

Date: 28/1/23

Subject: Mini Project 2B

Sem: VI

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13	Ganesh Patil	ganeshp8511@gmail.com	8451836588	Monitoring & Controlling	Prof. Nisha Shelar
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01	Tanvi Vijay Bhojane	bhojanetanvi20@gmail.com	7887609512	Implementation of Digital	
11	Sushant Rakesh More	Sushantmore110@gamail.com	8793299329	clock using FPGA	Prof. Vaishnavi Rajkor
14	Vikrant Vitthal Shirke	Vikrantshirke121@gmail.com	9325851272	0	
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90	Mamata Divesh Jangam	Mamtajangam88@gmail.com	9699530277	Implementation of FPGA in	Prof. Sumit Kumar
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Project Coordinator

Head of the Department

On

"FPGABASED HOME AUTOMATION USING

IOT"

Submitted in the partial fulfillment for the award of Bachelor of Engineering in

ELECTRONICS & TELECOMMUNICATION ENGINEERING

At

Atma Malik Institute of Technology & Research, Aghai



Submitted to

University of Mumbai, Mumbai

Prof. Sumit Kumar

(Project Guide)

Submitted by

SWAPNIL KUBADE

PARAG DESHMUKH

JYOTI KHARATMOL

DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING

ATMA MALIK INSTITUTE OF TECHNOLOGY & RESEARCH, AGHAI

DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that, "SWAPNIL KUBADE, PARAG DESHMUKH,

JYOTIKHARATMOL" have submitted a report on the basis of the project work
on "FPGA BASED HOME AUTOMATION USING IOT" of the subject Mini
Project 2A, Sem V as prescribed by the University of Mumbai in the college
during the academic year 2022-23.

Prof. Sumit Kumar

(Project Guide)

Prof. Sumit Kumar (HOD-EXTC) Or. D. D. Shind

(Principal)

External Examiner

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PROJECT REPORT

On

"IoT based Gas Leakage Monitoring & Controlling using FPGA"

Submitted in the partial fulfillment for the award of Bachelor of Engineering in

ELECTRONICS & TELECOMMUNICATION ENGINEERING

At

ATMA MALIK INSTITUTE OF TECHNOLOGY & RESEARCH, AGHAI



Submitted to

University of Mumbai, Mumbai

Prof- Nisha Shelar (Project Guide)

Submitted by

JAYESH ISAME ⁴
VISHAL NAIK
GANESH PATIL

DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING

ATMA MALIK INSTITUTE OF TECHNOLOGY & RESEARCH, AGHAI

DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that, "JAYESH ISAME, VISHAL NAIK, GANESH PATIL" has submitted a report on the basis of the project work on "IoT based Gas Leakage Monitoring & Controlling using FPGA" as a partial fulfillment towards the completion of "Sixth Semester" of course in Engineering in the branch of Electronics and Telecommunication during the academic year 2022-23.

(Project Guide)

Prof. Sumit Kumar (HOD-EXTC) Dr. D. D. Shinde (Principal)

External Examiner

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On

"FPGA BASED FIVE AXIS ROBOT ARM CONTROLLER"

Submitted in the partial fulfillment for the award of Bachelor of Engineering in

ELECTRONICS & TELECOMMUNICATION ENGINEERING

At

Atma Malik Institute of Technology & Research, Aghai



Submitted to

University of Mumbai, Mumbai

Prof. Sumit kumar

(Project Guide)

Submitted by

PRATIK NIMBA SONAR ANIKET VITTHAL YERUNKAR SAYALI SHANKER BHOSALE

DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING

2022-23

ATMA MALIK INSTITUTE OF TECHNOLOGY & RESEARCH, AGHAI

DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that, "PRATIK NIMBA SONAR, ANIKET VITTHAL YERUNKAR, SAYALI SHANKAR BHOSALE" have submitted a report on the basis of the project work on "FPGA BASED FIVE AXIS ROBOT ARM CONTROLLER" of the subject Mini Project 2A, Sem VI as prescribed by the University of Mumbai in the college during the academic year 2022-23.

Prof Sumit kumar

(Project Guide)

Prof. Sumit Kumar

(HOD-EXTC)

Dr. D. D. Shinde

(Principal)

External Examiner



On

"Implementation of Digital Clock Using FPGA"

Submitted in the partial fulfillment for the award of Bachelor of Engineering in

ELECTRONICS & TELECOMMUNICATION ENGINEERING

At

Atma Malik Institute of Technology & Research, Aghai



Submitted to
University of Mumbai, Mumbai

Nisha Shelar

(Project Guide)

Submitted by

Vikrant Shirke

Tanvi Bhojane

Sushant More

DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING

ATMA MALIK INSTITUTE OF TECHNOLOGY & RESEARCH, AGHAI

DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that, "Vikrant Shirke, Tanvi Bhojane, Sushant More" have submitted a report on the basis of the project work on "Implementation Of Digital Clock Using FPGA" of the subject Mini Project 2A, Sem 6th as prescribed by the University of Mumbai in the college during the academic year 2022-23.

Nisha Shelar. (Project Guide) Prof. Sumit Kumar (HOD-EXTC)

Dr. D. D. Shinde (Principal)

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On

"IMPLEMENTATION OF FPGA IN FINGERPRINT BASED AUTHENTICATION SYSYTEM"

Submitted in the partial fulfillment for the award of Bachelor of Engineering in

ELECTRONICS & TELECOMMUNICATION ENGINEERING

At

Atma Malik Institute of Technology & Research, Aghai



Submitted to

University of Mumbai, Mumbai

Prof. Sumit Kumar

(Project Guide)

Submitted by

SANDESH S. MHASE

TUSHAR S. KADAM

AKSHATA A. HALDE

MAMATA D. JANGAM

DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING

ATMA MALIK INSTITUTE OF TECHNOLOGY & RESEARCH, AGHAI

DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION **ENGINEERING**



CERTIFICATE

This is to certify that, "Sandesh, Tushar, Akshata, Mamata" have submitted a report on the basis of the project work on

"IMPLEMENTATION OF FPGA IN FINGERPRINT BASED AUTHENTICATION SYSYTEM"

of the subject Mini Project 2A, Sem VI as prescribed by the University of Mumbai in the college during the academic year 2022-23.

Prof. Sumit Kumar (Project Guide)

Prof. Sumit Kumar (HOD-EXTC)

Dr. D. D. Shinde (Principal)

External Examiner

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